

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET ECH8649 — General-Purpose Switching Device **Applications**

Features

- · Low ON-resistance
- Best suited for LiB charging and discharging switch
- Halogen free compliance
- 2.5V drive
- · Common-drain type
- · Protection diode in

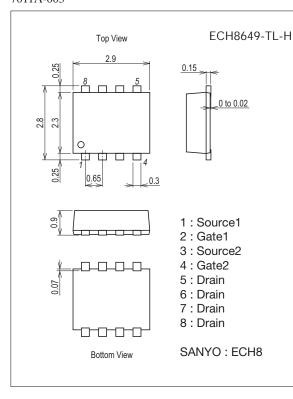
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	۱ _D		7.5	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	40	А
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm ² x0.8mm) 1unit	1.4	W
Total Power Dissipation	PT	When mounted on ceramic substrate (900mm ² ×0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7011A-003



Product & Package Information

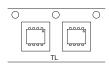
: ECH8

• JEITA, JEDEC : -

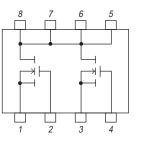
• Minimum Packing Quantity : 3,000 pcs./reel

Packing Type : TL

Package



Electrical Connection





http://semicon.sanyo.com/en/network

Marking

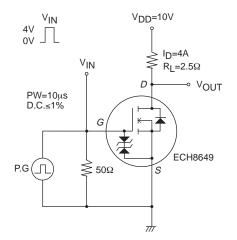
LOT No.

WТ

Electrical Characteristics at Ta=25°C

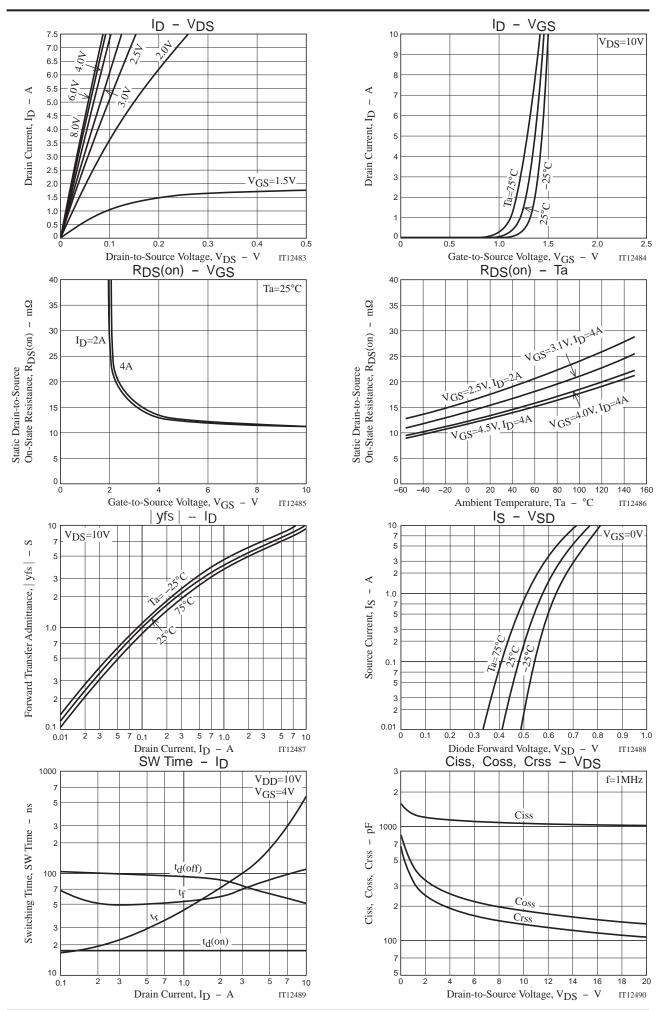
Parameter	Cumbal	Conditions	Ratings			1114	
Parameter	Symbol	Conditions	min	typ	max	Unit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	20			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ	
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	0.5		1.3	V	
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =4A	4.2	7		S	
	R _{DS} (on)1	ID=4A, VGS=4.5V		13	17	mΩ	
Statia Drain to Course On State Decistones	R _{DS} (on)2	ID=4A, VGS=4.0V	9.4	13.5	18	mΩ	
Static Drain-to-Source On-State Resistance	R _{DS} (on)3	ID=4A, VGS=3.1V	11	16	22	mΩ	
	RDS(on)4	ID=2A, VGS=2.5V	12.5	18	26	mΩ	
Input Capacitance	Ciss			1060		pF	
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		180		pF	
Reverse Transfer Capacitance	Crss			135		pF	
Turn-ON Delay Time	t _d (on)			17.5		ns	
Rise Time	tr			120		ns	
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		68		ns	
Fall Time	tf			80		ns	
Total Gate Charge	Qg			10.8		nC	
Gate-to-Source Charge	Qgs	VDS=10V, VGS=4.5V, ID=7.5A		2.1		nC	
Gate-to-Drain "Miller" Charge	Qgd			2.9		nC	
Diode Forward Voltage	V _{SD}	IS=7.5A, VGS=0V		0.74	1.2	V	

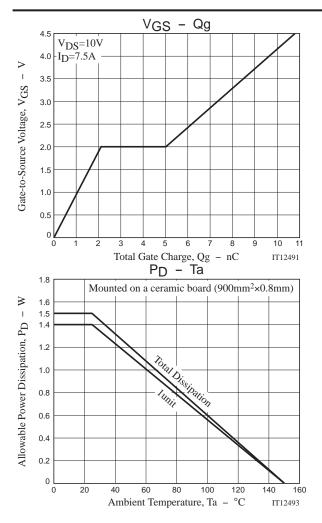
Switching Time Test Circuit

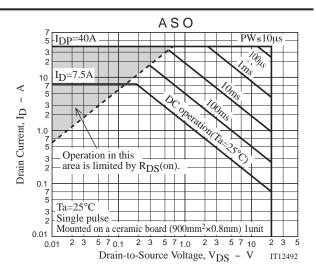


Ordering Information

0				
Device Package		Shipping	memo	
ECH8649-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free	







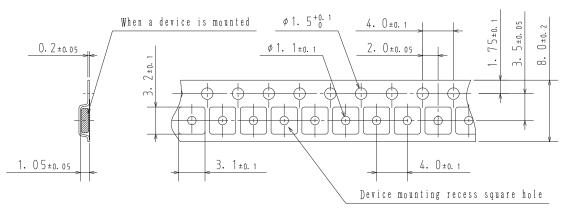
Embossed Taping Specification ECH8649-TL-H

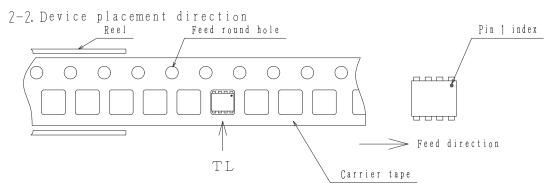
1. Packing Format

Package Name			ximum Number of ces contained (pcs)		Packing format		
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1) Outer BOX (A-7)		
ECH8	CPH6	3,000	15,000	90,000	5 reels contained 6 inner boxes contained		
	_				Dimensions:mm (external) Dimensions:mm (external)		
					183×72×185 440×195×210		
<u>Reel label, Inner box label</u> Un it:mm) <u>(un it:mm)</u> <u>Outer box label</u> It is a label at the time of factory shipments The form of a label may change in physical distribution process.							
	Type LOT Quan Orig	No. tity		TYPE O O O 11 <td>000000 108 000000 ************************************</td>	000000 108 000000 ************************************		
Reel label The LEAD FREE * description shows that the surface treatment of the terminal is lead free.			of the terminal is lead free.				
				Label LEAD FRI LEAD FRI	CE 3 JEITA Phase 3A		

2. Taping configuration

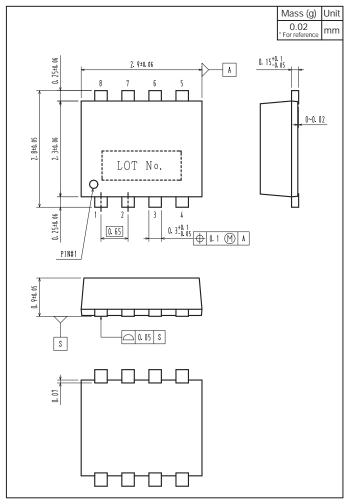
2-1. Carrier tape size (unit:mm)



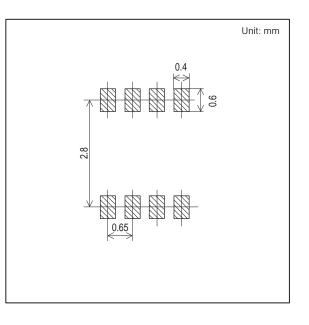


Those with pin 1 index on the feed hole side ·····TL

Outline Drawing ECH8649-TL-H



Land Pattern Example



Note on usage : Since the ECH8649 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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